	Type	L #	Hits	Search Text	DBs	Time Stamp
12	BRS	L37	666	(pld pla fpga asic (programmable adj logic)) and (configuration adj memory) and reconfigur$5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/21 15:30
13	BRS	L38	290	37 and (part$5 near3 (configur$5 reconfigur$5))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/21 15:32
14	BRS	L39	235	38 and (configuration adj data)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/21 15:32
15	BRS	L40	90	39 and (memory near3 interfac$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/21 15:33